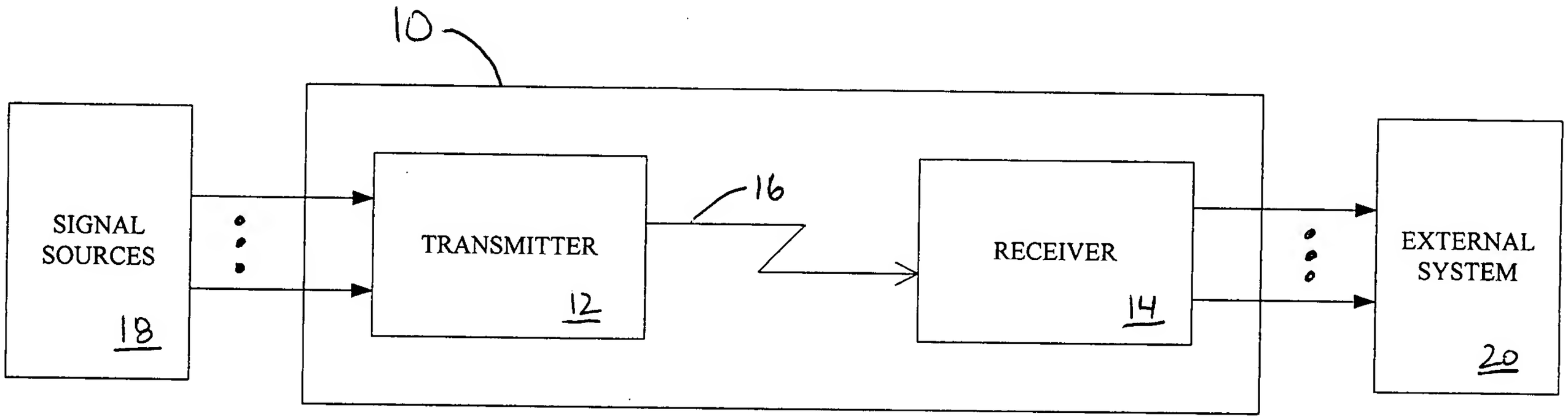
10
SIGNAL SOURCES
18
TRANSMITTER
12
16
RECEIVER
14
EXTERNAL SYSTEM
20

FIGURE 1

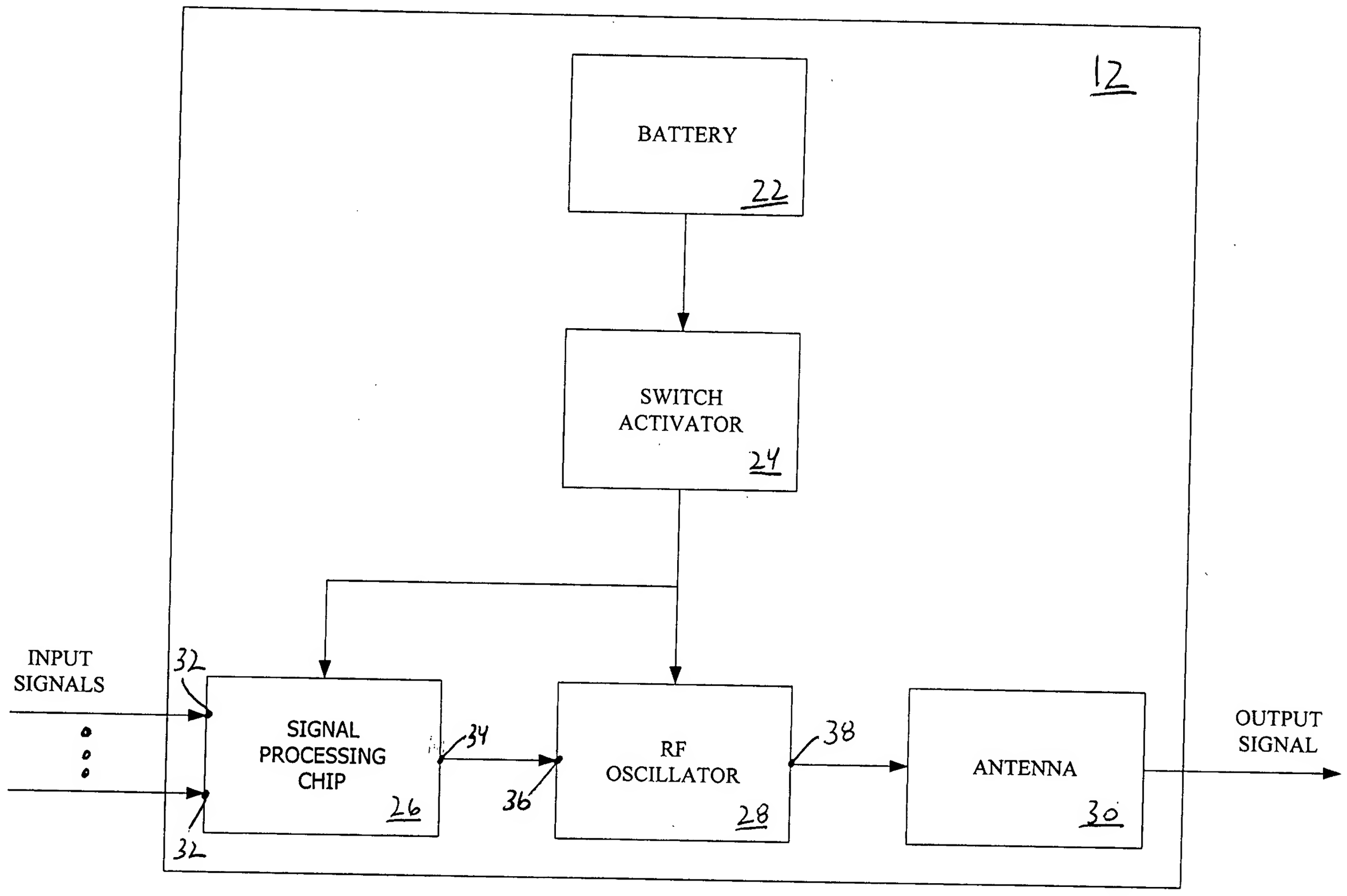
12
BATTERY
22
SWITCH
ACTIVATOR
24
INPUT
SIGNALS
32
SIGNAL
PROCESSING
CHIP
26
34
36
RF
OSCILLATOR
28
38
ANTENNA
30
OUTPUT
SIGNAL
32

FIGURE 2

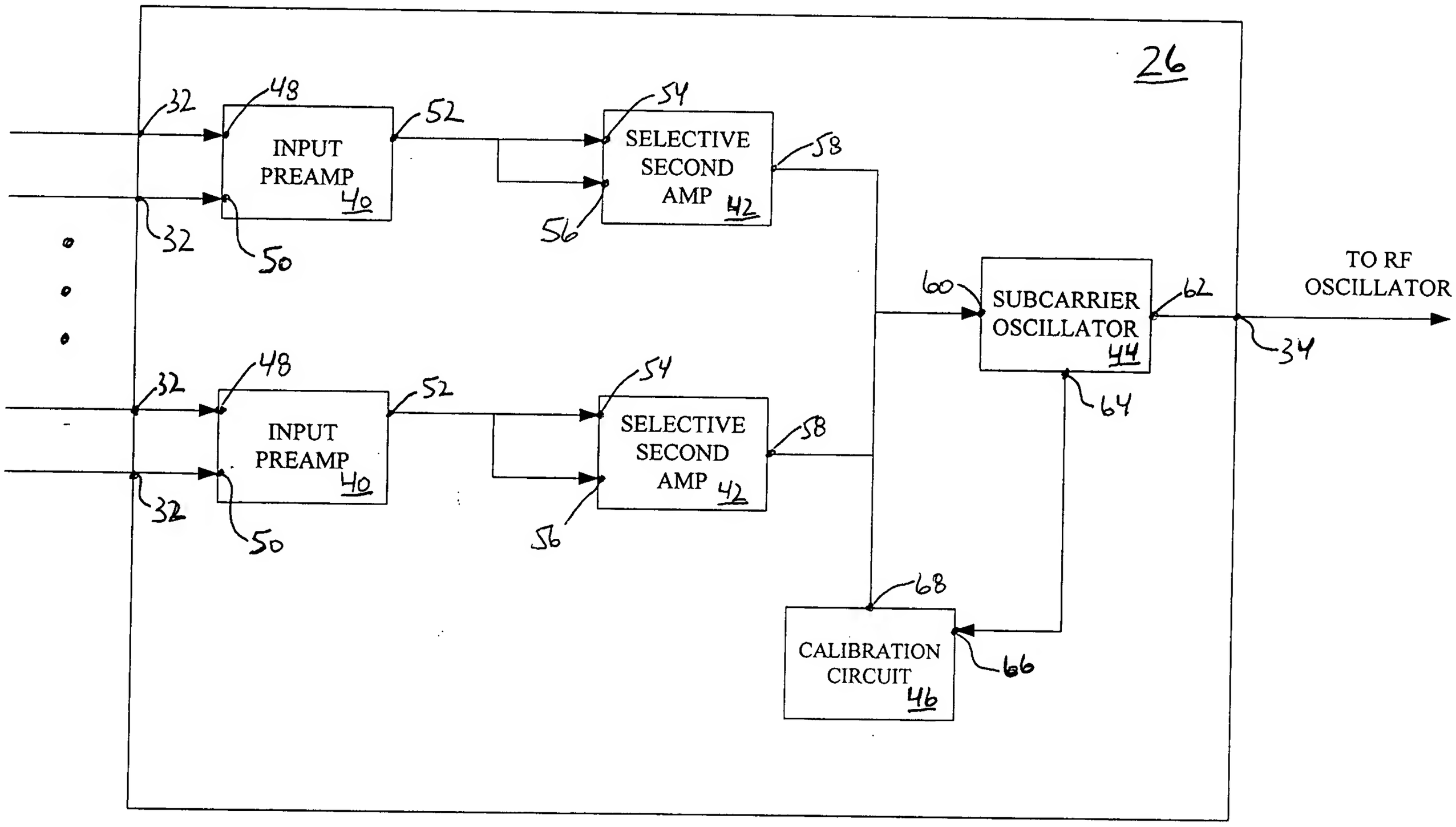
26
32
48
INPUT PREAMP 40
52
54
SELECTIVE SECOND AMP 42
58
56
50
60
SUBCARRIER OSCILLATOR 44
62
TO RF OSCILLATOR
34
64
68
CALIBRATION CIRCUIT 46
66

FIGURE 3

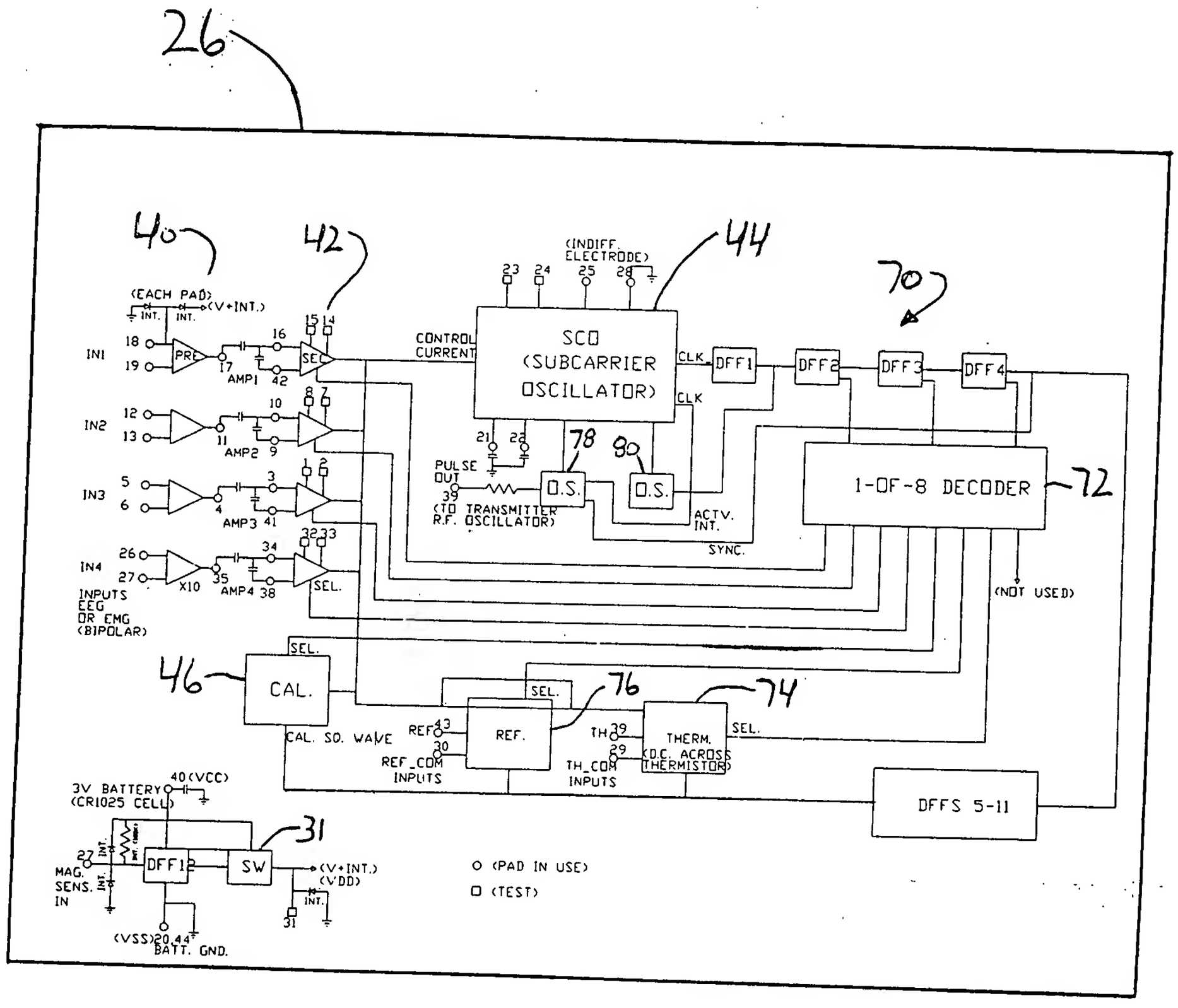
26
40
42
44
70
72
74
76
78
90
(EACH PAD)
INT.
INT.
(V+INT.)
IN1
IN2
IN3
IN4
18
19
12
13
5
6
26
27
PRE
SEC
AMP1
AMP2
AMP3
AMP4
X10
16
17
42
10
11
9
3
4
41
34
35
38
32
33
SEL.
INPUTS
EEG
OR EMG
(BIPOLAR)
(INDIFF. ELECTRODE)
23
24
25
28
CONTROL CURRENT
SCO
(SUBCARRIER OSCILLATOR)
CLK
DFF1
DFF2
DFF3
DFF4
21
22
PULSE OUT
39
(TO TRANSMITTER R.F. OSCILLATOR)
O.S.
O.S.
ACTV. INT.
SYNC.
1-OF-8 DECODER
(NOT USED)
46
CAL.
CAL. SQ. WAVE
REF
43
30
REF_COM
INPUTS
REF.
TH
39
29
TH_COM
INPUTS
THERM.
(D.C. ACROSS THERMISTOR)
DFFS 5-11
3V BATTERY
(CR1025 CELL)
40 (VCC)
27
MAG. SENS. IN
DFF12
SW
(V+INT.)
(VDD)
31
(VSS) 20, 44
BATT. GND.
O (PAD IN USE)
□ (TEST)

FIGURE 4

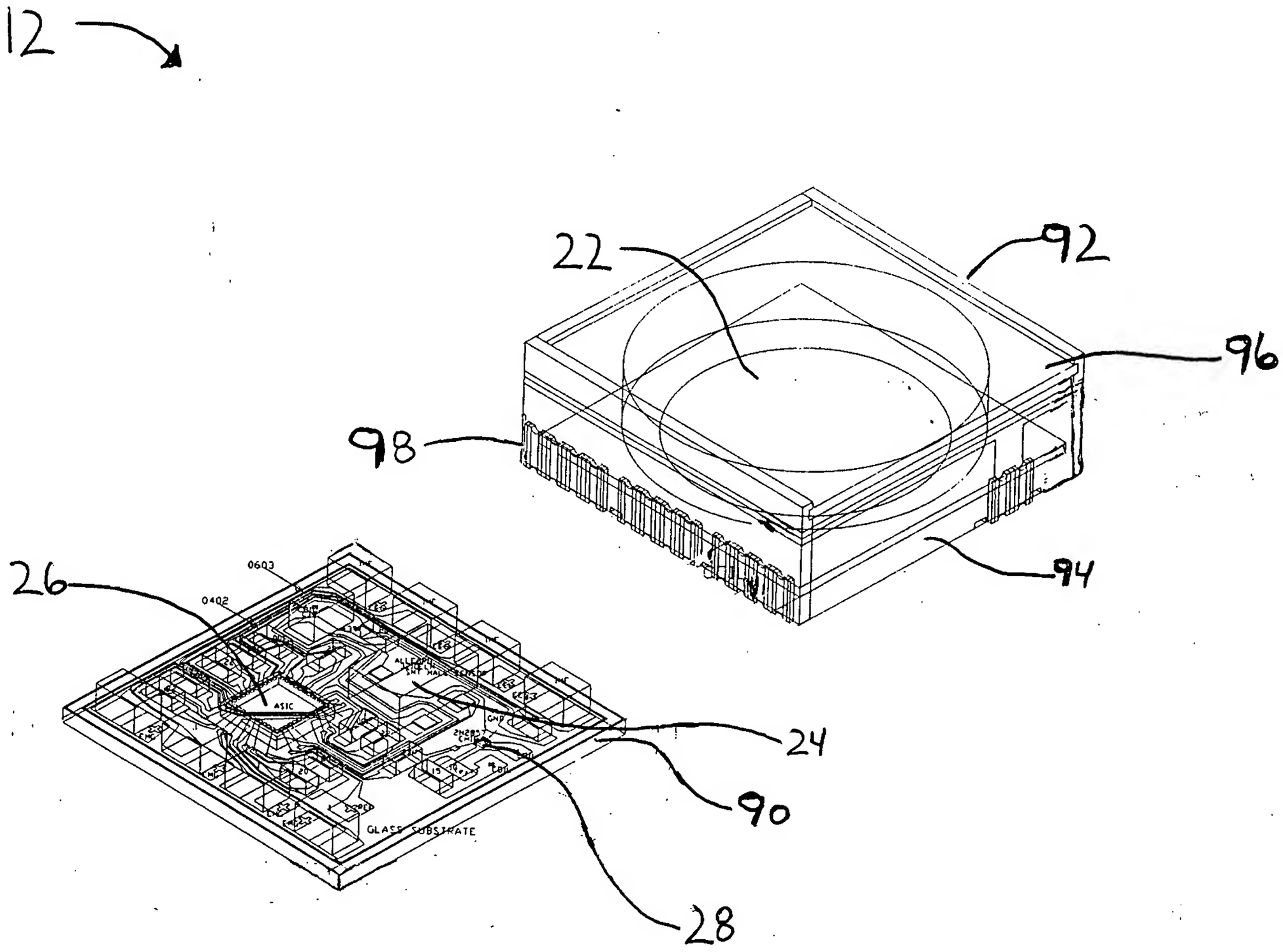

FIGURE 5

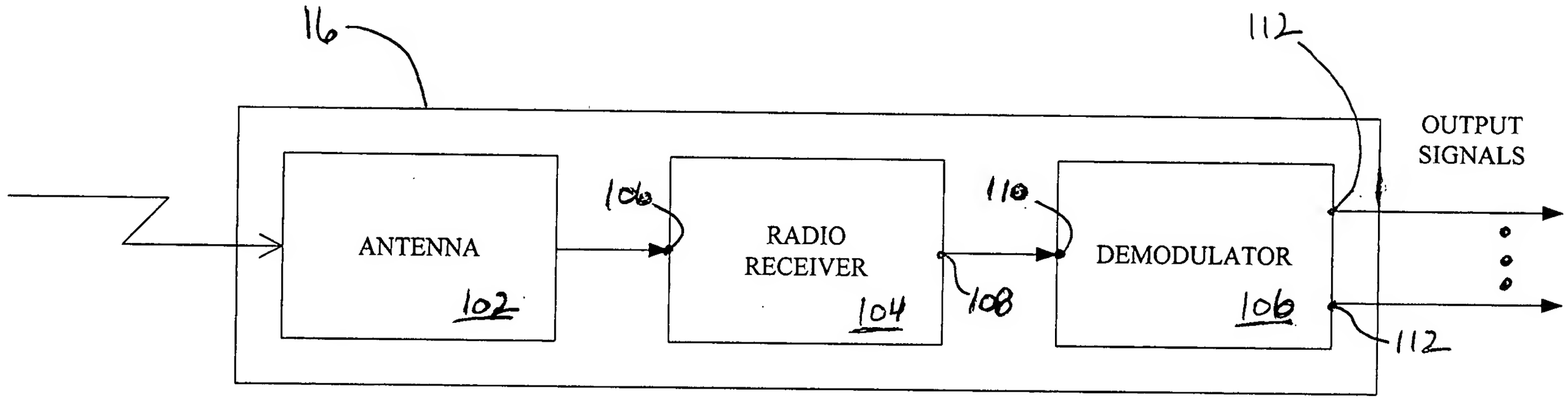
16
112
OUTPUT
SIGNALS
ANTENNA
102
106
RADIO
RECEIVER
104
108
110
DEMODULATOR
106
112

FIGURE 6

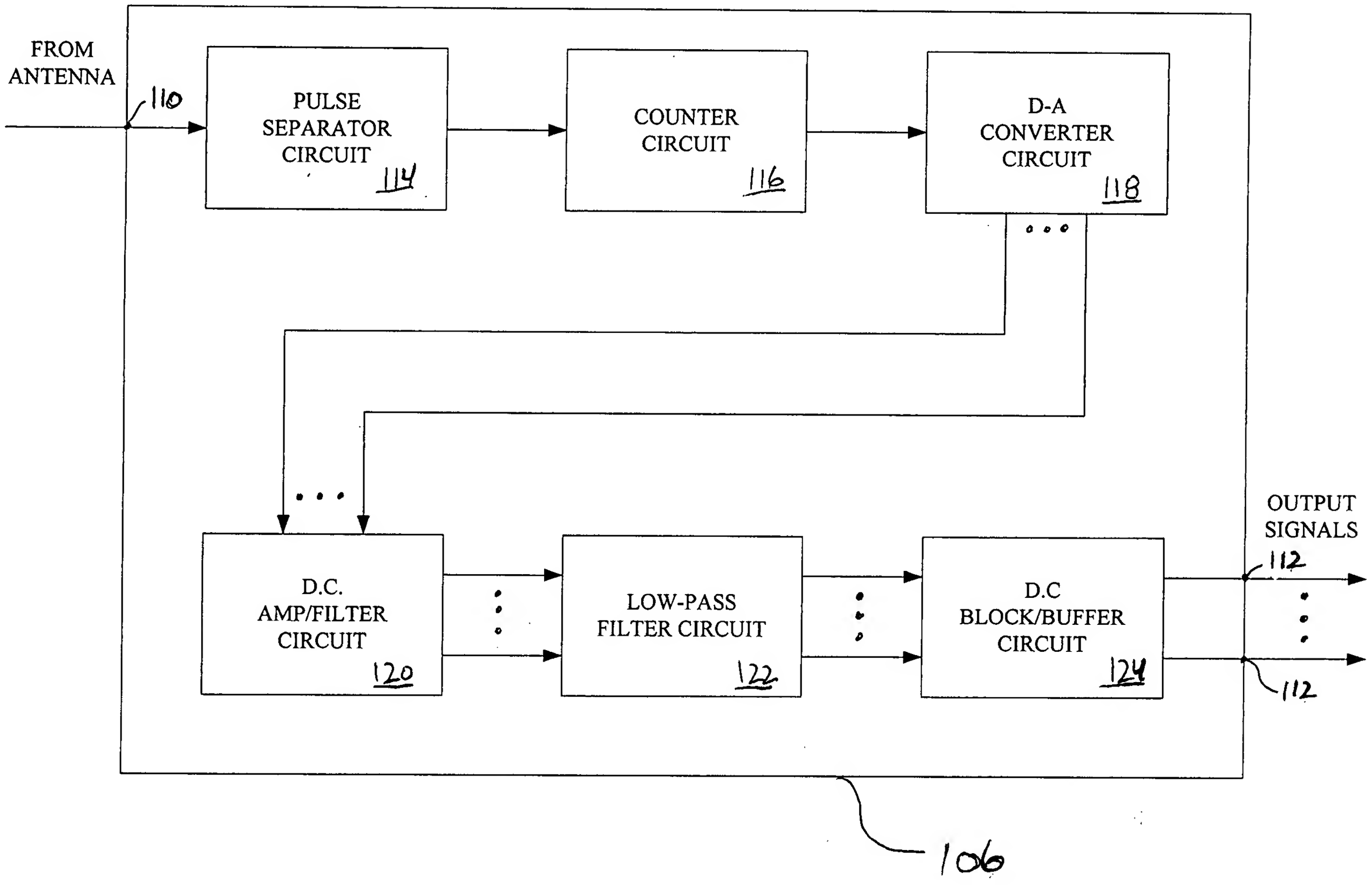
FROM ANTENNA
110
PULSE SEPARATOR CIRCUIT
114
COUNTER CIRCUIT
116
D-A CONVERTER CIRCUIT
118
D.C. AMP/FILTER CIRCUIT
120
LOW-PASS FILTER CIRCUIT
122
D.C BLOCK/BUFFER CIRCUIT
124
OUTPUT SIGNALS
112
112
106

FIGURE 7

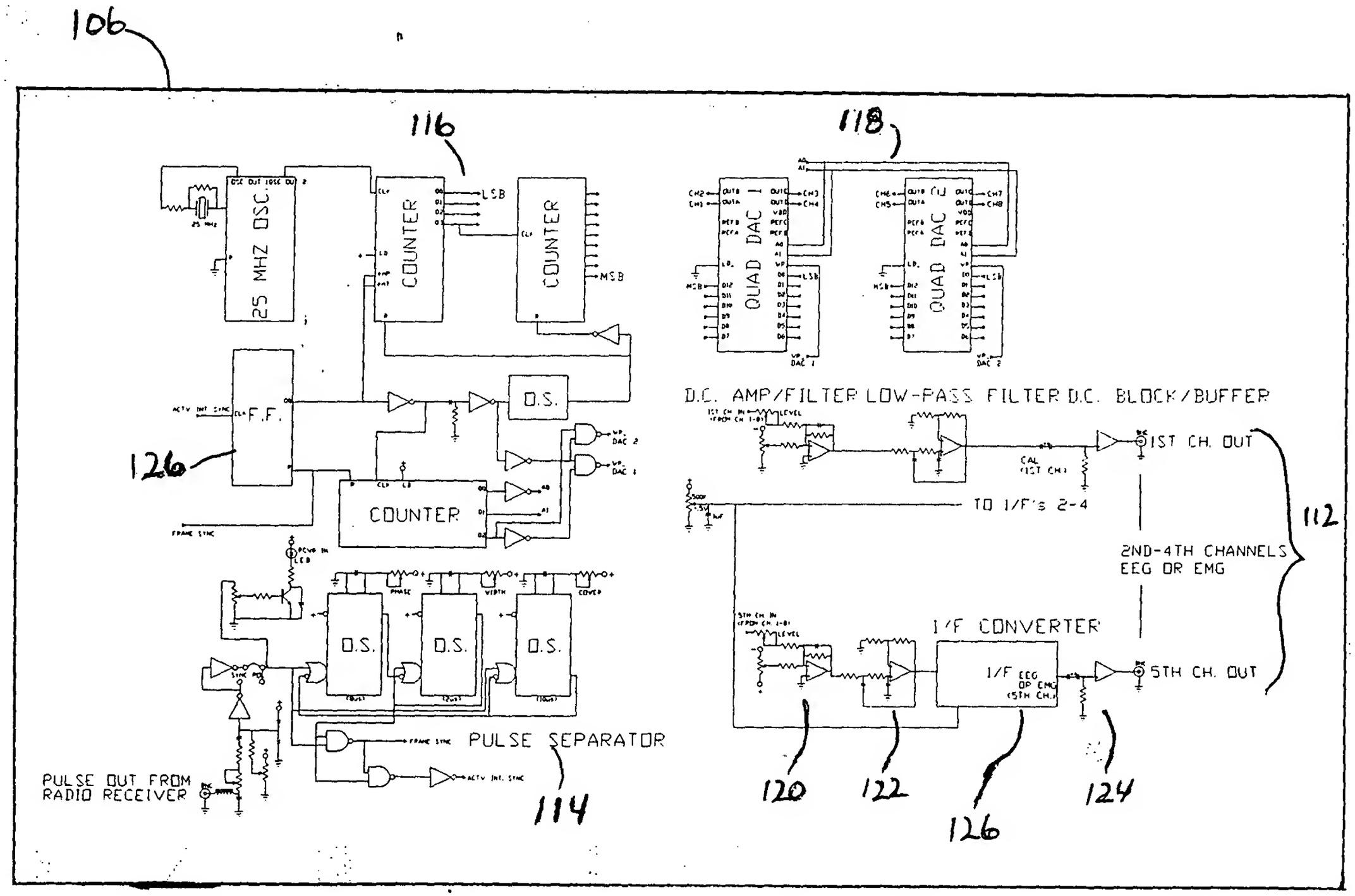
106
116
118
25 MHZ OSC.
COUNTER
COUNTER
LSB
MSB
QUAD DAC 1
QUAD DAC 2
F.F.
126
O.S.
COUNTER
O.S.
O.S.
O.S.
PULSE SEPARATOR
114
PULSE OUT FROM RADIO RECEIVER
D.C. AMP/FILTER
LOW-PASS FILTER
D.C. BLOCK/BUFFER
1ST CH. OUT
TO I/F's 2-4
2ND-4TH CHANNELS EEG OR EMG
112
I/F CONVERTER
I/F EEG OR EMG (5TH CH.)
5TH CH. OUT
120
122
126
124

FIGURE 8

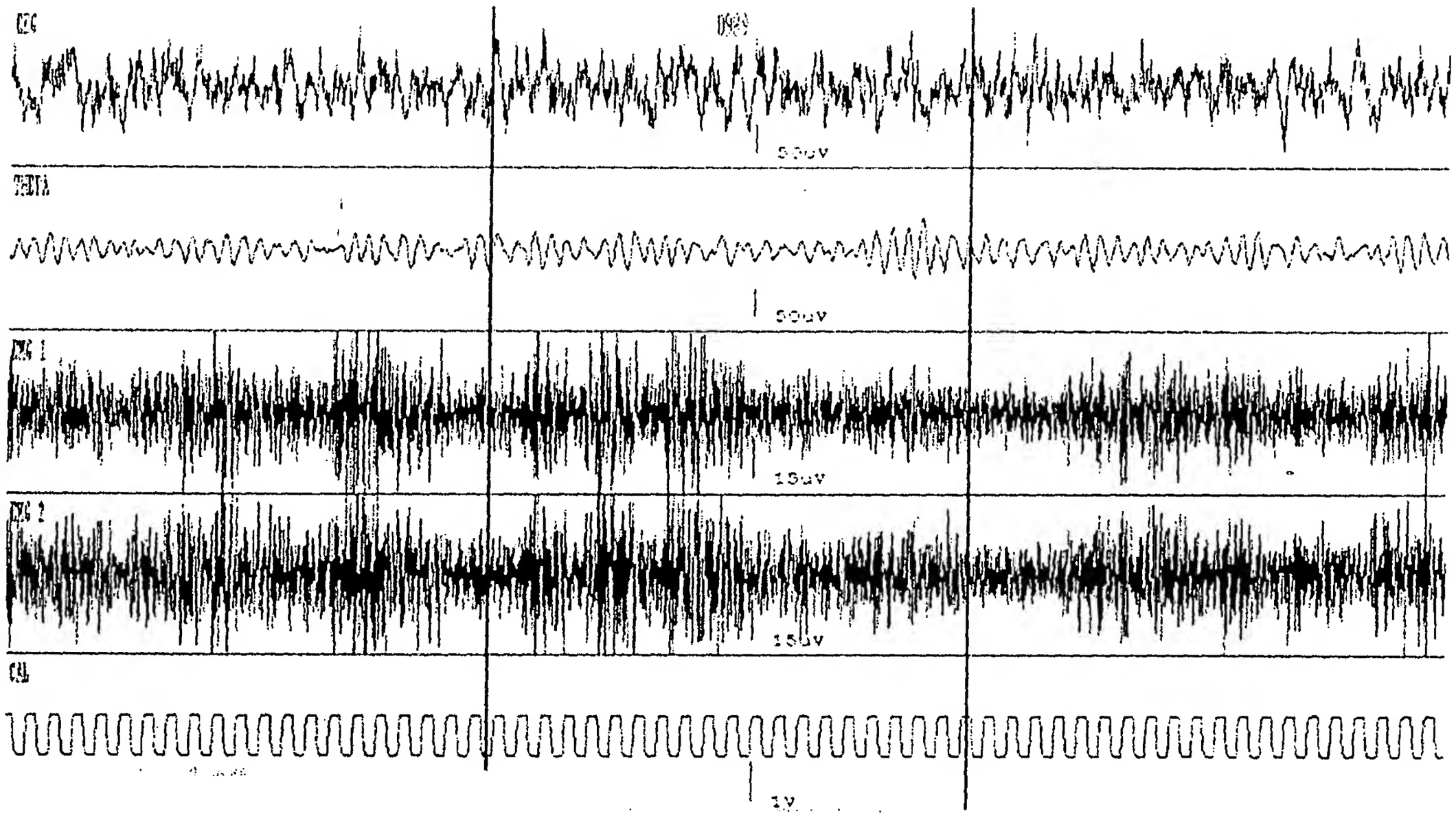

FIGURE 9